

a' --32. The system of claim 31 further comprising means for modulating, wherein said means for modulating receives said second clock at said second frequency and receives said second plurality of video lines in said first video format, wherein said means for modulating converts said first video format into a second video format.--

--33. The system of claim 32 wherein said first video format is selected from the group consisting of VGA and SVGA.--

--34. The system of claim 32 wherein said second video format is selected from the group consisting of NTSC, PAL, SECAM, and SCART.--

--35. The system of claim 32 wherein said first video format is SVGA and said second video format is NTSC.--

--36. The system of claim 30 wherein said first frequency is an integer ratio of said second frequency.--

--37. The system of claim 30 wherein said first frequency is a non-integer ratio of said second frequency.--

$a' -$ 

--39. The system of claim 30 wherein said first clock and said second clock are asynchronous.--

**THE**